Netra™ CP2040 CompactPCI Board Read Me First

Sun Microsystems, Inc.
901 San Antonio Road
Palo Alto, CA 94303-4900 U.S.A.
650-960-1300

Part No.816-0610-10
October 2001 Revision A

Send comments about this document to: docfeedback@sun.com

Please
Recycle

Netra™ CP2040 CompactPCI Board Read Me First

This *Read Me First* document is written for users of the Netra CP2040 CompactPCI board. The Netra CP2040 boards are designed as host processors for application processing in High Availability CompactPCI carrier-grade systems. The Netra CP2040 boards can function as System Host or Satellite Host boards. For a feature list of the CP2040 please see the *Netra CP2040 Technical Reference and Installation Manual (P/N 806-4994-xx).*

Check the Contents of Your Netra CP2040 CompactPCI Board Shipment

Be sure to check the contents of your package.

Please keep the Sun Microsystems™ packaging as this will be required for the return of any failed units for repair. If any of the items are missing or damaged, contact your Sun Microsystems field application engineer (FAE). This document is shipped with the following CP2040 board assembly (see FIGURE 1):

- Assembly, CP2040S-500 (P/N 501-5721-*xx*)

A Certificate of Paid Royalty for Embedded Use of Solaris™ Operating Environment is included (P/N 816-2663-10, Revision A).

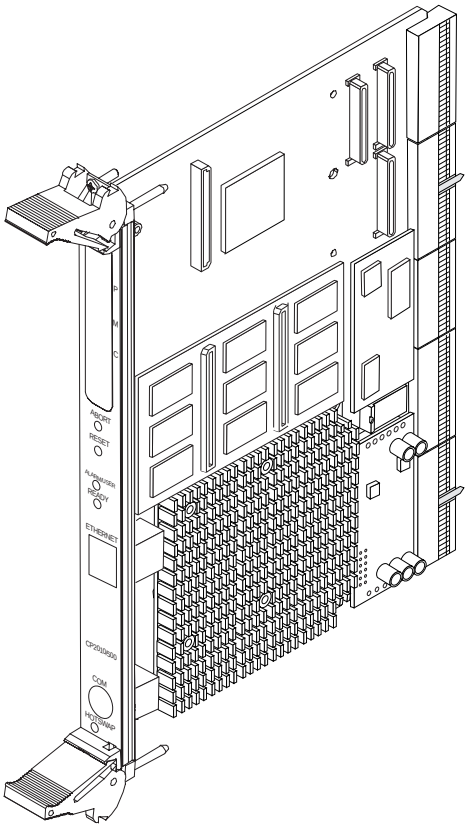

FIGURE 1 Illustration of a Typical Netra CP2040 Board

Note – The CP2040 board part number and board serial number are identified on sticker labels. Please see FIGURE 2 for their locations.

Safety Considerations

For information on safety considerations, refer to the *Netra CP2040 Technical Reference and Installation Manual (P/N 806-4994-xx).* A copy may be obtained from your local FAE.

Software Information

The Solaris™ 8 1/01 operating environment is supported by the CP2040 board. The *CP 2000 Supplemental CD 3.1 for Solaris™ 8* contains additional features for the CP2040 board. For information on how to obtain the CD, please refer to the product Website:

http://www.sun.com/microelectronics/boards/cp/2040/

Hardware Information

The SMC module, the heatsink, the power module, and the front panel are an integral part of the CP2040 board. Any attempt to remove these components from the board will void your warranty. Please see the *Netra CP2040 Technical Reference and Installation Manual (P/N 806-4994-xx)* to determine the location of these components.

Related Documentation

Please refer to the *Netra CP2040 Technical Reference and Installation Manual (P/N 806-4994-xx)* for comprehensive technical details. A copy may be obtained from your local FAE or from the product Website:

http://www.sun.com/microelectronics/boards/cp/2040/.

Technical Support

If you have any technical questions or technical support issues that are not addressed in the Netra CP2040 documentation set or you need to return the CP2040 board, please read the details below or contact your local field applications engineer (FAE).

Please have the board identification information ready (see FIGURE 2). The CP2040 board part number, board serial number, product part number, product dash, date code and MAC address can be found on stickers located on the board labels. For proper identification of the board, please see the list below and FIGURE 2. The Sun Microsystems barcode label provides the following information:

- Board part number (for example, 5015721) which is the first seven digits on the barcode label. The next six digits are the board serial number (for example, 000230).
- Product part number (for example, 5621), product dash (for example, 04) and revision number (for example REV 09).
- Date code (for example, 08/2001) is the eighth week of the year 2001.
- Country of origin (for example, ASSEMBLED IN CANADA)
- MAC (media access control) address for your board.

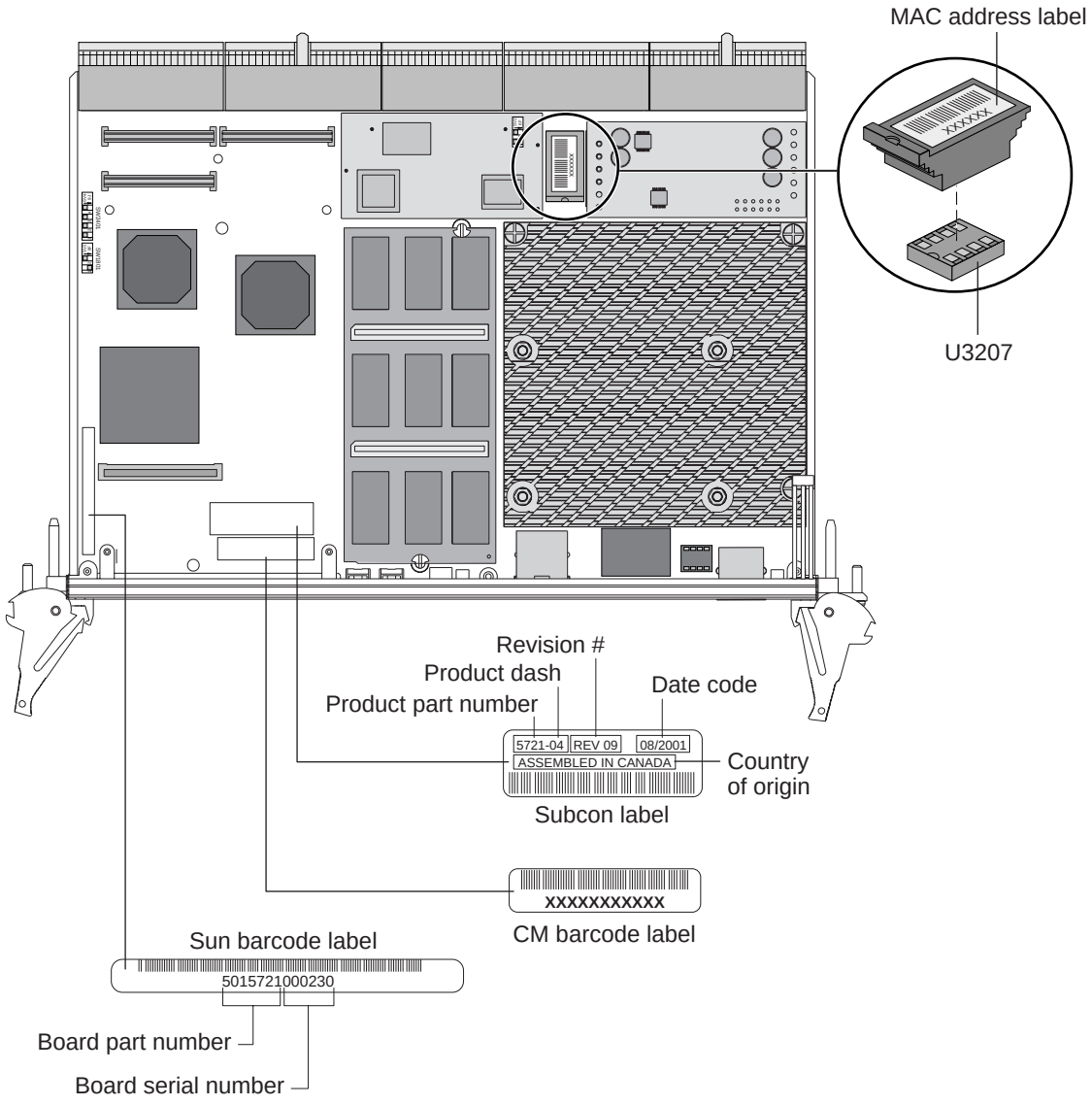

FIGURE 2 Identification Labels and Serial I^2C EEPROM on a Typical Netra CP2040

Caution – Ensure that you are installing a CP2040 board by checking to see that its part number is 5015721, as shown in FIGURE 2.

Replacing the Serial EEPROM

The Serial I^2C EEPROM is the MAC address carrier and it stores the backup copy of the board MAC address and Host ID information. The CP2040 board supports the Serial I^2C EEPROM.

If you need to replace the CP2040 board, remove the Serial I^2C EEPROM from the original board and install it on the new CP2040 board. To correctly position the Serial I^2C EEPROM and to precisely install it on the CP2040 board, see FIGURE 2.

Installation of the Board into a Non-High Availability (Non-HA) Chassis

Note – The CP2040 board is configured for operation in a non-HA chassis by default. If you are unsure of your settings, follow the procedure below.

Before Installing the Board in a Chassis

If you have a non-HA chassis, set the SMC module switch settings (on SW0501) as follows *before powering* on the board (see FIGURE 3 and FIGURE 4):

- SW0501.Switch1 ---> Closed (switch is set in direction of arrow)
- SW0501.Switch2 ---> Closed (switch is set in direction of arrow)

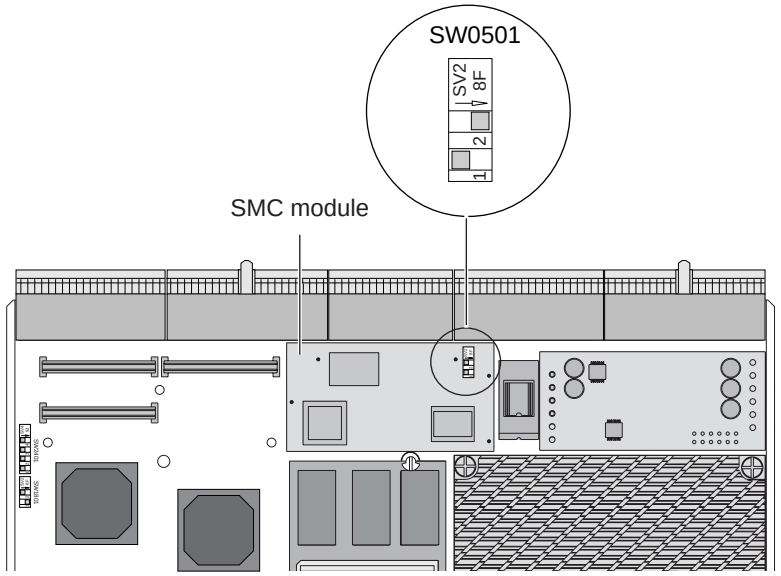

FIGURE 3 Location of SW0501 on SMC Module

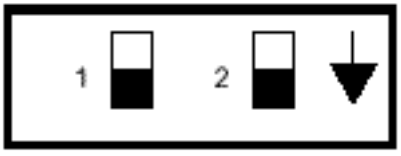

FIGURE 4 Switch SW0501 in Closed Position (Default) for Non-HA Operation

Installation of the Board into a High Availability (HA) Chassis

Note – The CP2040 board is configured for operation in a non-HA chassis by default. If you need to change the settings for HA operation, follow the procedure below.

Before Installing the Board in a Chassis

If you are installing the board into an HA chassis, the SMC module switch settings (SW0501) need to be set as follows (see FIGURE 5 and FIGURE 6).

- SW0501.Switch1 ---> Open (switch is set *opposite* to arrow direction)
- SW0501.Switch2 ---> Closed (switch is set in direction of arrow)

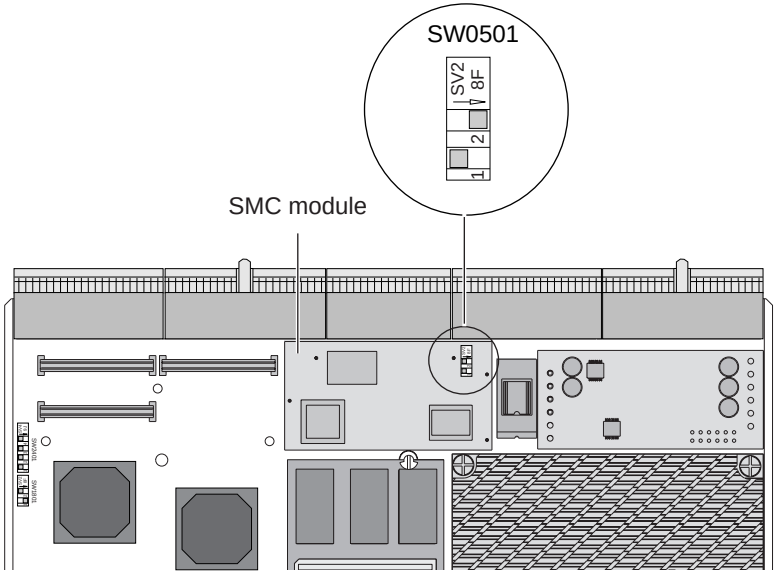

FIGURE 5 Location of SW0501 on SMC Module

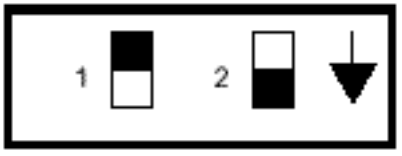

FIGURE 6 Switch SW0501 Setting for Operation in HA Chassis